

Notice of References Cited	Application/Control No. 10/761,187	Applicant(s)/Patent Under Reexamination KISHIRO, KOICHI	
	Examiner Christy L. Novacek	Art Unit 2822	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	C	US-6,242,337	06-2001	Okada, Norio	438/622
	D	US-6,214,722	04-2001	Lin et al.	438/637
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S. and Tauber, R.N., "Silicon Processing for the VLSI Era", Vol. 1, pp. 445, 1986
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.